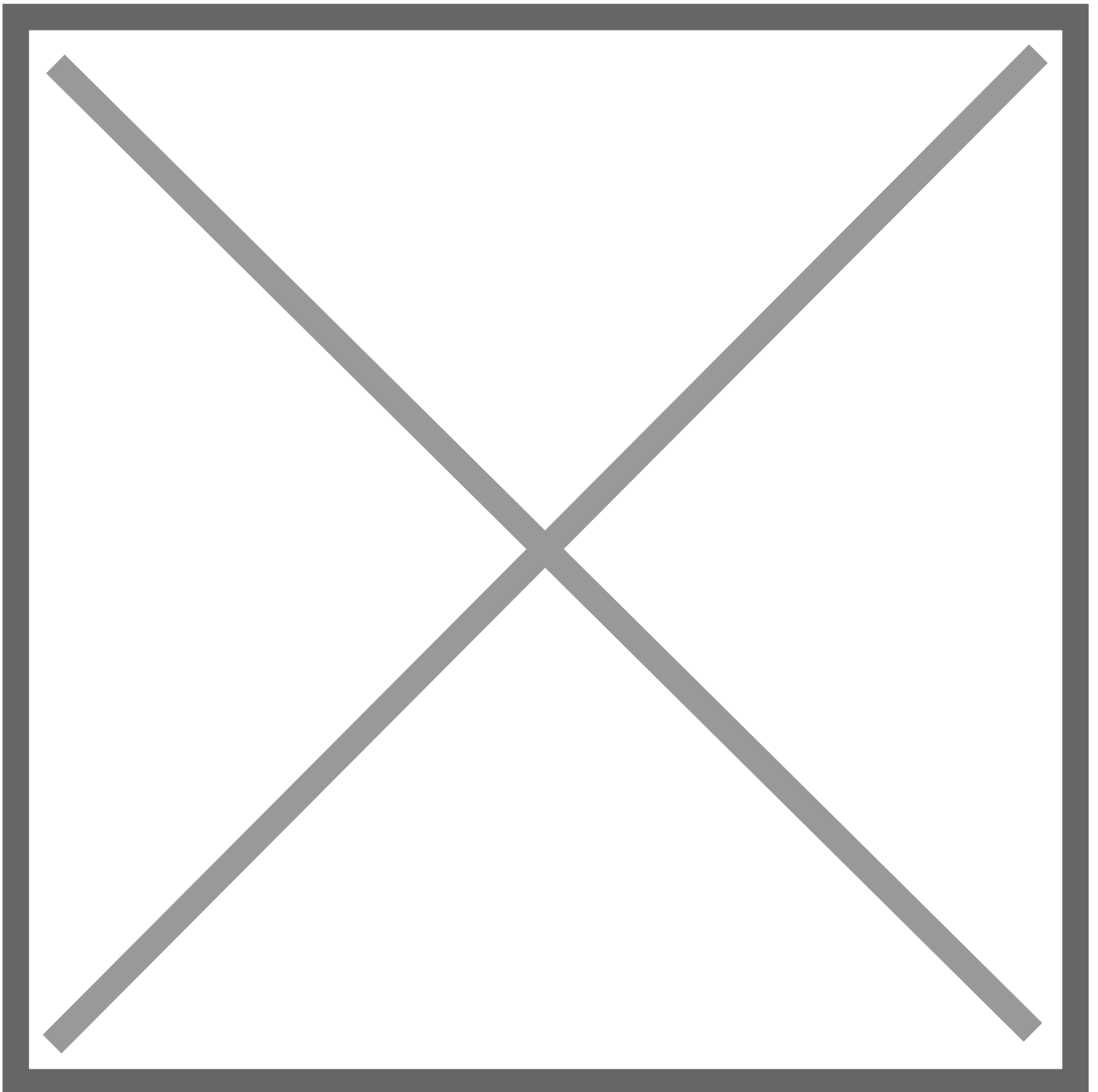


Project Simulation

- Click the **Simulate tab** on the top menu.
- Select **Advanced Simulation**.
- In advanced mode, click on **Menu and then Run Iverilog**.
- **Check the message that appears:**
 - If there are no errors, your design is ready for the next step.
 - If there are errors, adjust the blocks and connections and repeat the process.



Revision #1

Created 3 April 2025 19:17:03 by Caroline

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