

# Wrapping Up

**Congratulations!** You have successfully created a **basic FPGA CAN network project** in ChipInventor. We covered:

- Setting `can_controller_std` timing parameters.
- Using `uart_can_std_printer` to convert CAN frames into readable text over UART.
- Handling reset signals and interrupts via `inverterC` and a `one_hz_clock` that provides periodic triggers.

From here, you can **branch out into more advanced CAN projects**, adding ID filters, adjusting data rates, and integrating with external devices in **industrial or automotive** CAN networks. If you have questions, review your diagram and CAN parameters, and enjoy exploring new projects in ChipInventor!

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