

Simulating the circuit

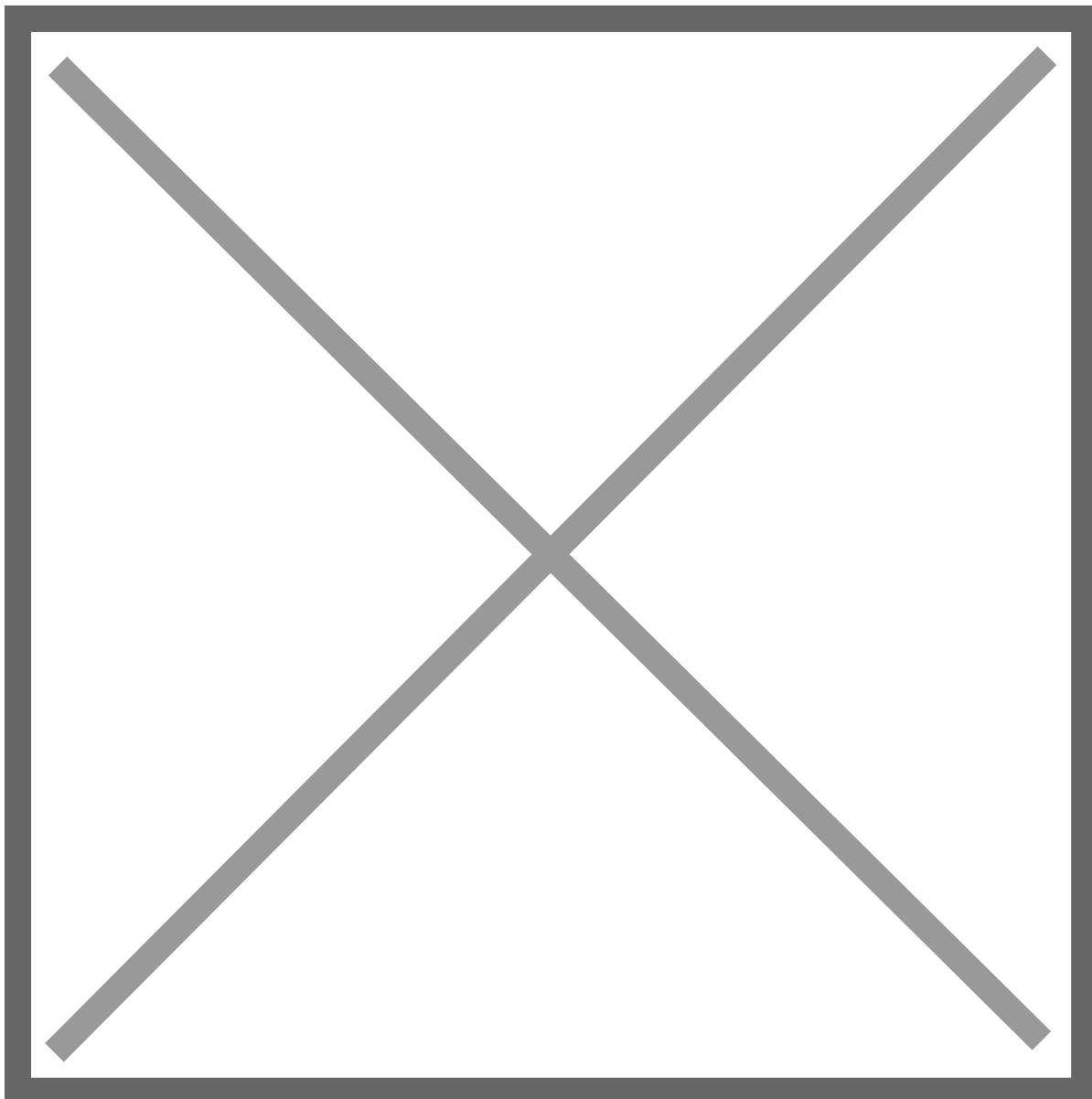
Now we need to simulate the MUX circuit and observe how the selection input determines the output.

1. Go to the Simulate tab and select Dynamic Simulation.

2. The Verilog code will load automatically.

3. Click Run to execute the simulation:

- Use the interactive menu to change the input values (D0, D1, and S).
- Observe the output (Y) to verify the behavior of the MUX



You can experiment with different combinations of inputs to fully understand the MUX functionality.

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