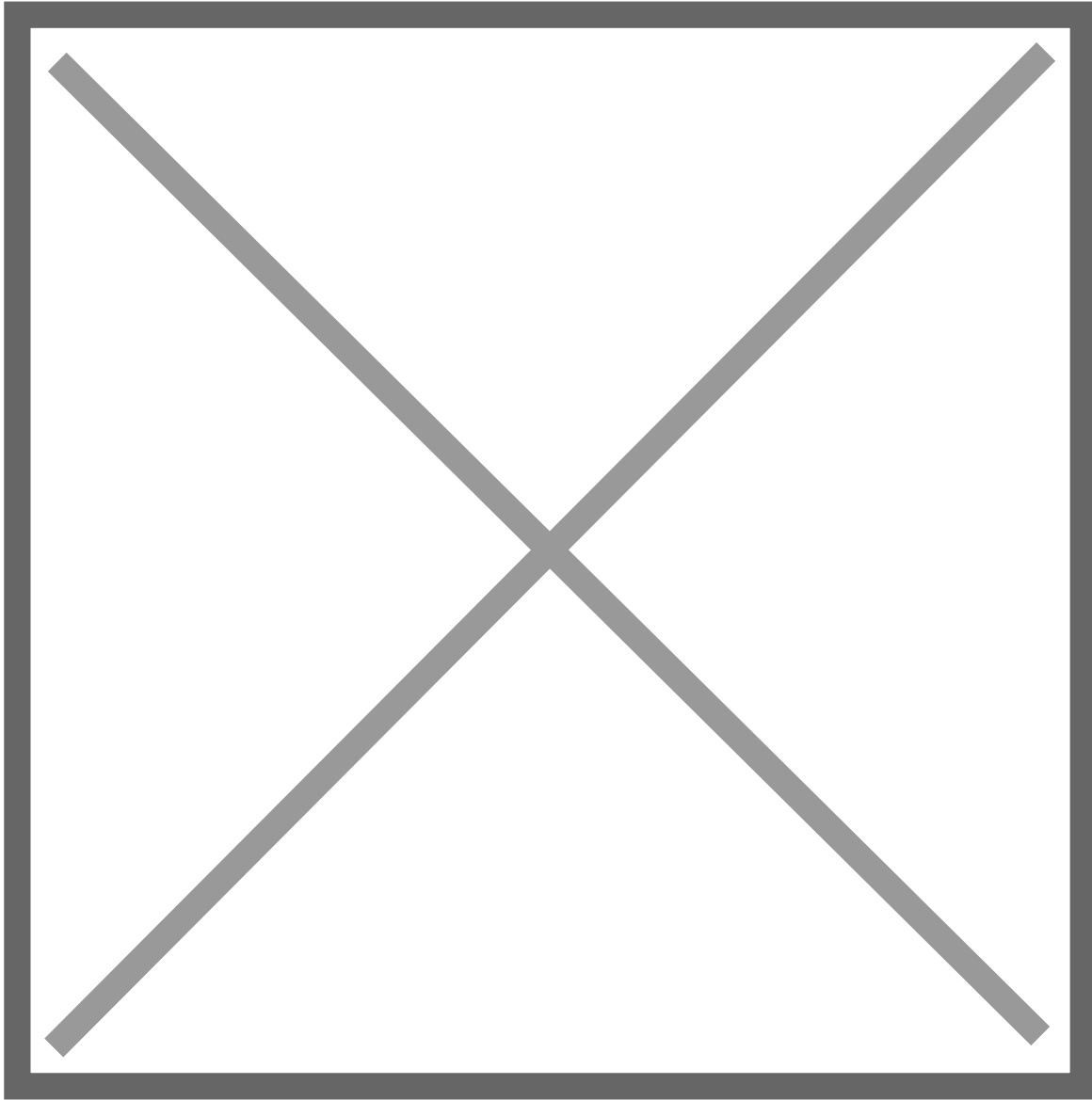
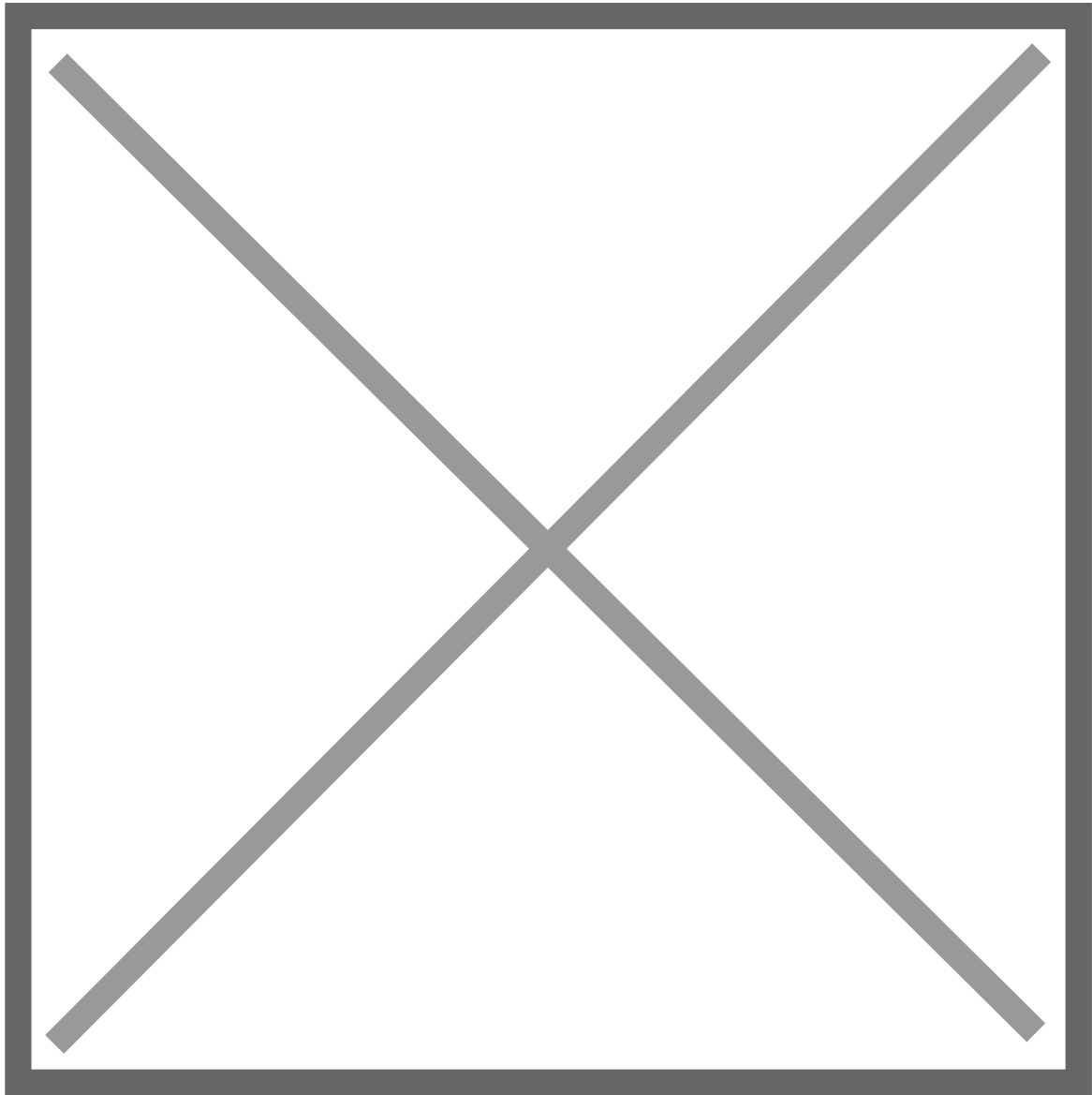


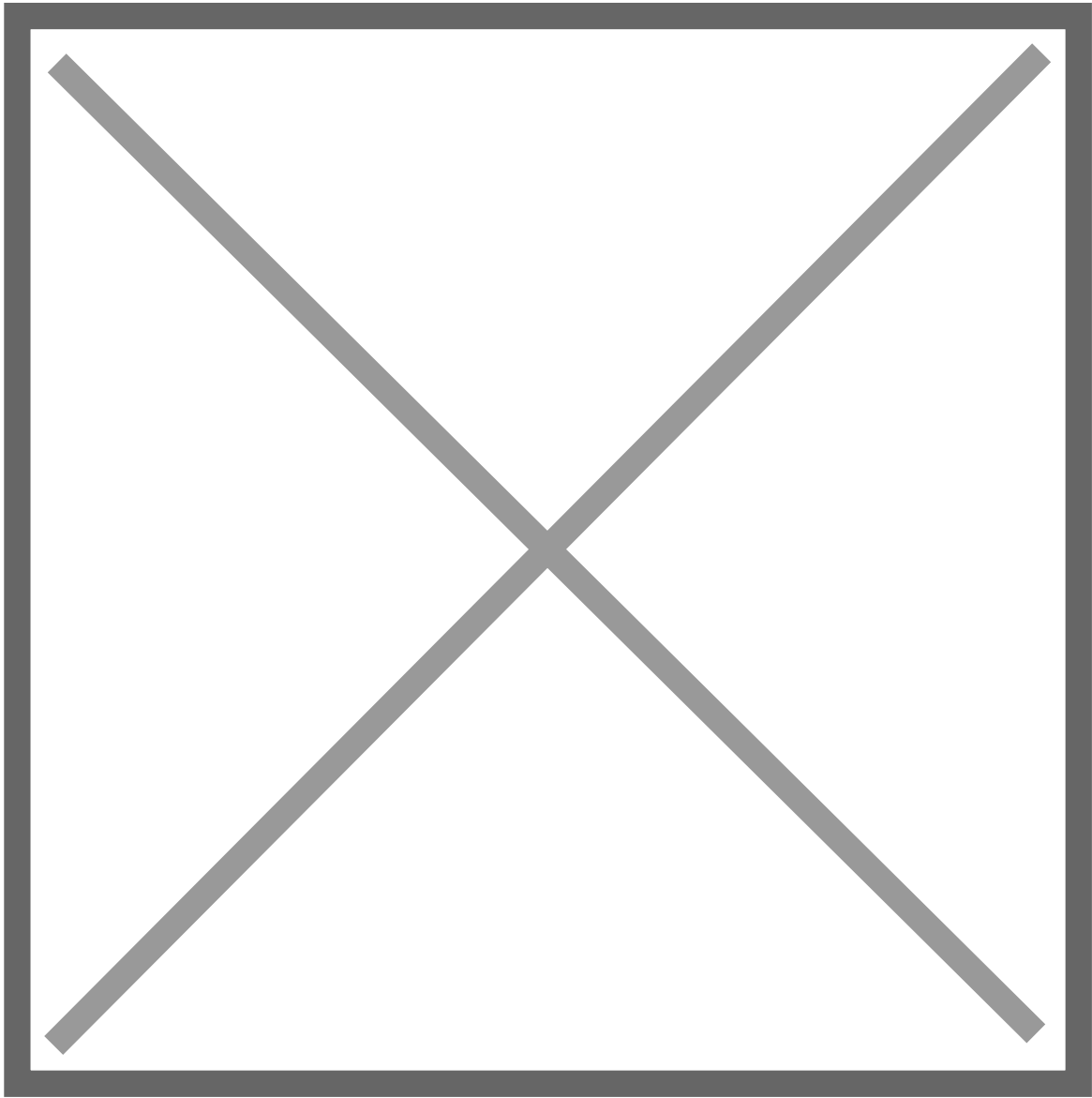
# Visual Resources



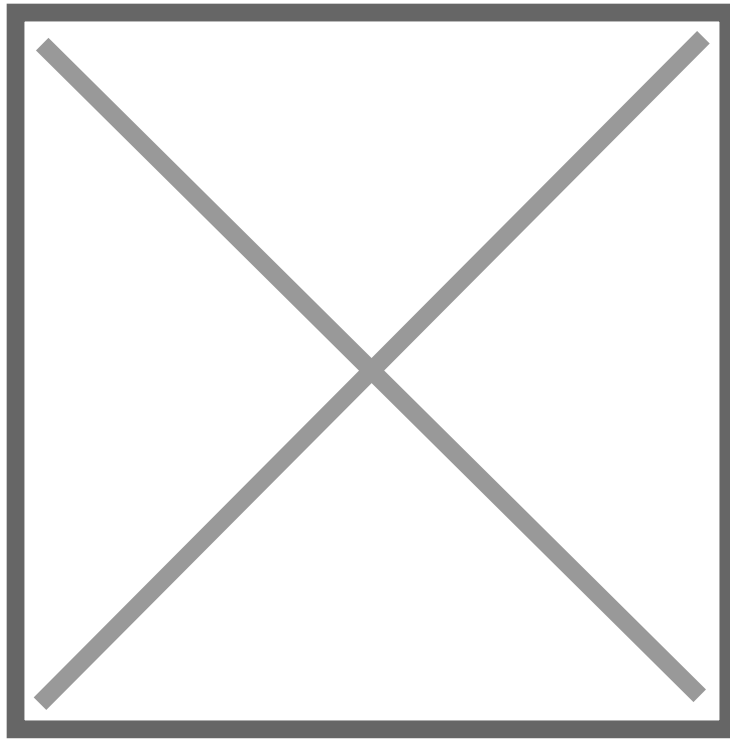
**Figure 1: Connections in the FPGA**



**Figure 2: General structure - 64-point FFT**



**Figure 3: Processing Module**



**Figure 4: Butterfly-shaped multiplication and addition structure**

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Revision #3

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