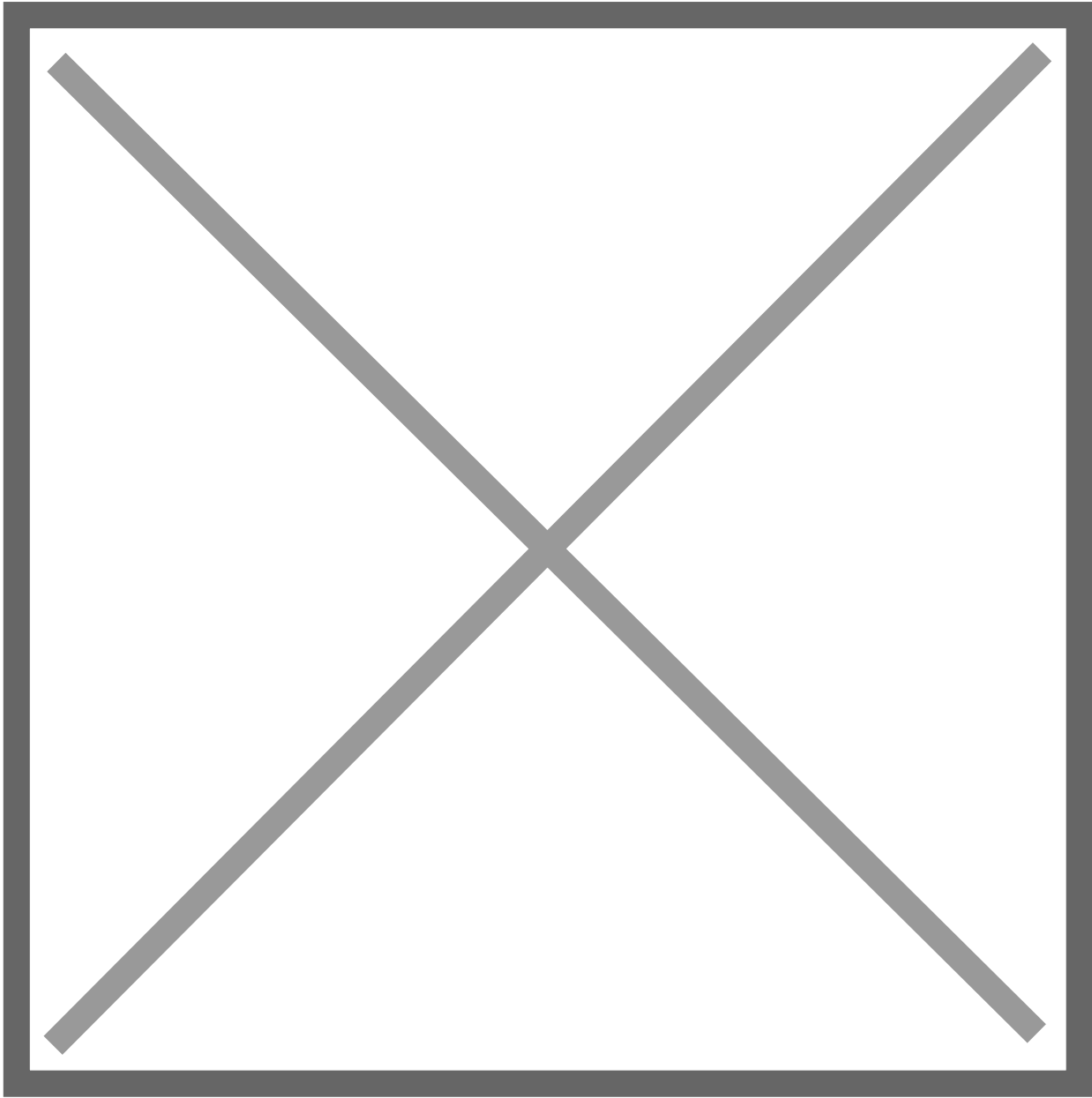


Simulation Stage

Before programming the FPGA, it's essential to simulate the circuit to ensure the behavior is correct.

Simulation Steps:

1. Access the Simulate tab in the top menu.
2. Select Dynamic Simulation.
3. What to Check During Simulation:
 - When IO69 or IO68 change state, the output of the corresponding demux2 switches between outa and outb.
 - The outputs reflect the expected behavior of the motors (or LEDs for testing).



Revision #2

Created 17 March 2025 12:30:18 by Caroline

Updated 17 March 2025 12:30:56 by Caroline