

Hardware Validation

With the FPGA programmed, test the behavior:

1. Connect an LED to the output pin led0.

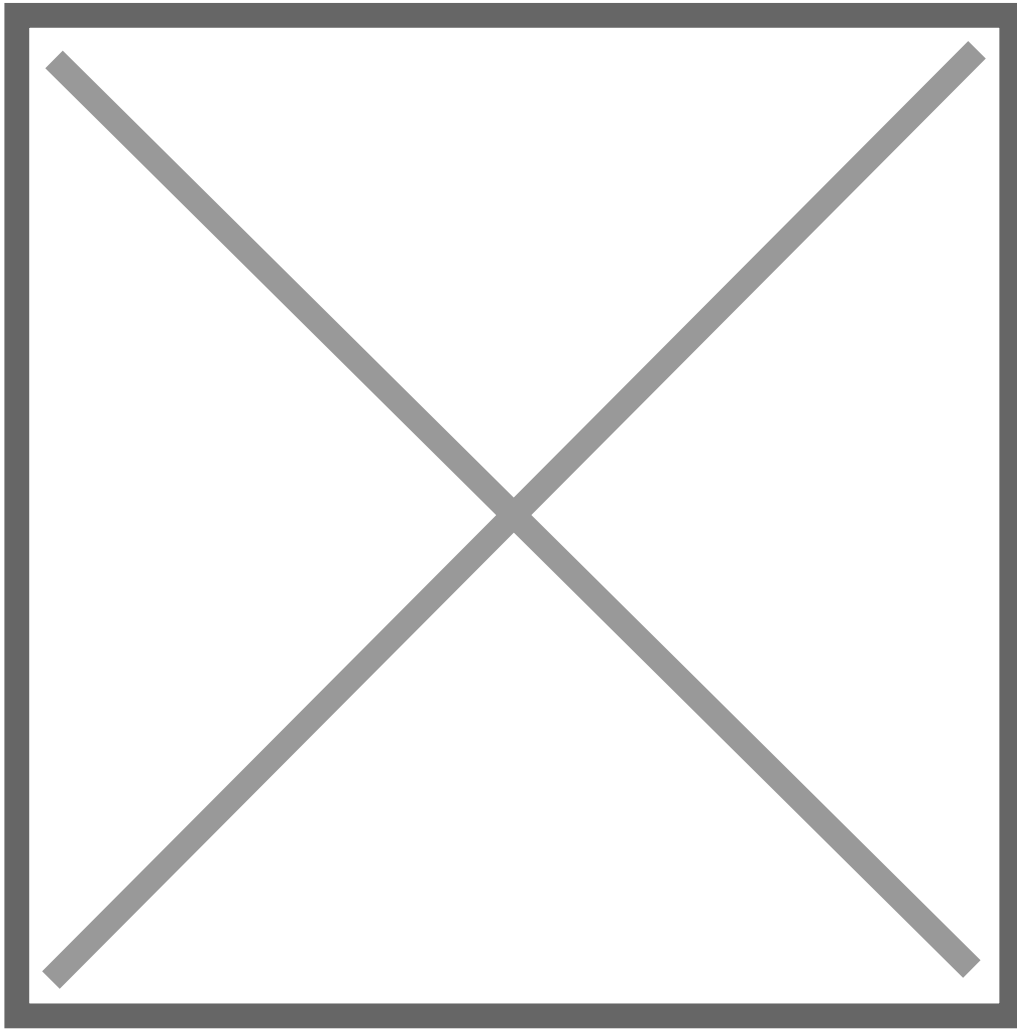
2. Observe the LED brightness:

- It should blink at a fixed frequency, with brightness gradually increasing (duty cycle increasing).
- Once the counter reaches the maximum value (255), the PWM resets.

3. If the behavior is not as expected:

- Check your block connections in ChipInventor.
- Ensure the system clock is properly connected.





Revision #2

Created 26 March 2025 19:45:43 by Caroline

Updated 27 March 2025 18:42:47 by Caroline