Visual Resources

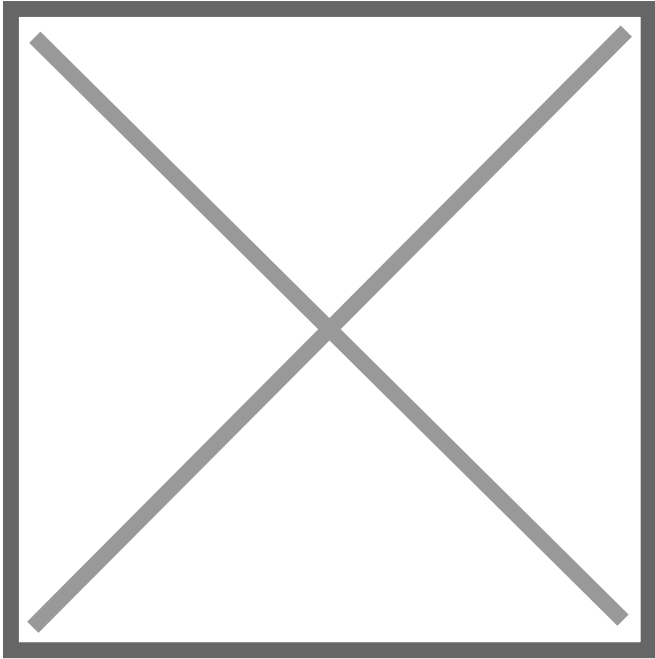**Figure 1: Connections in the FPGA**
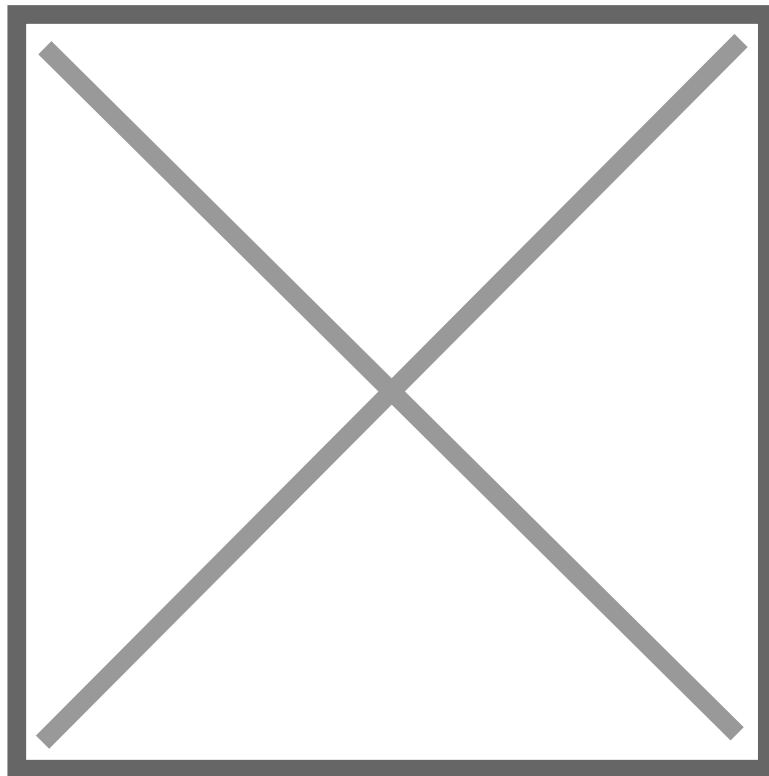

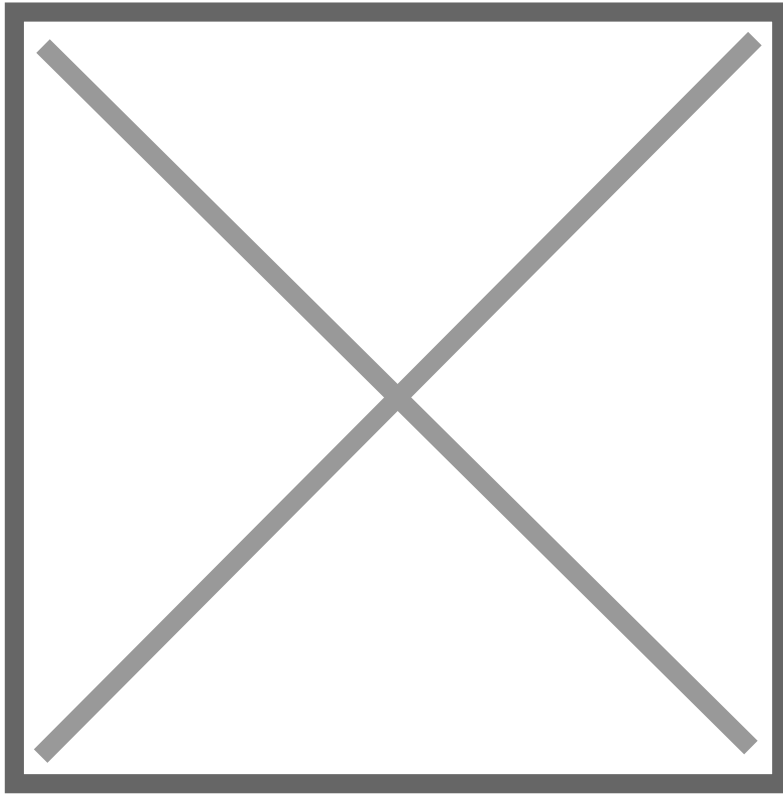**Figure 2: Camera**

Figure 3: Camera