

Visual Resources

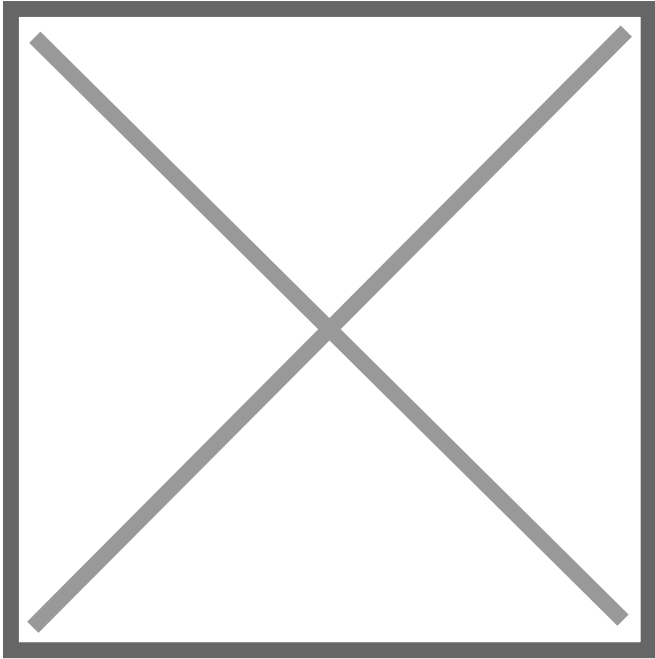


Figure 1: Connections in the FPGA

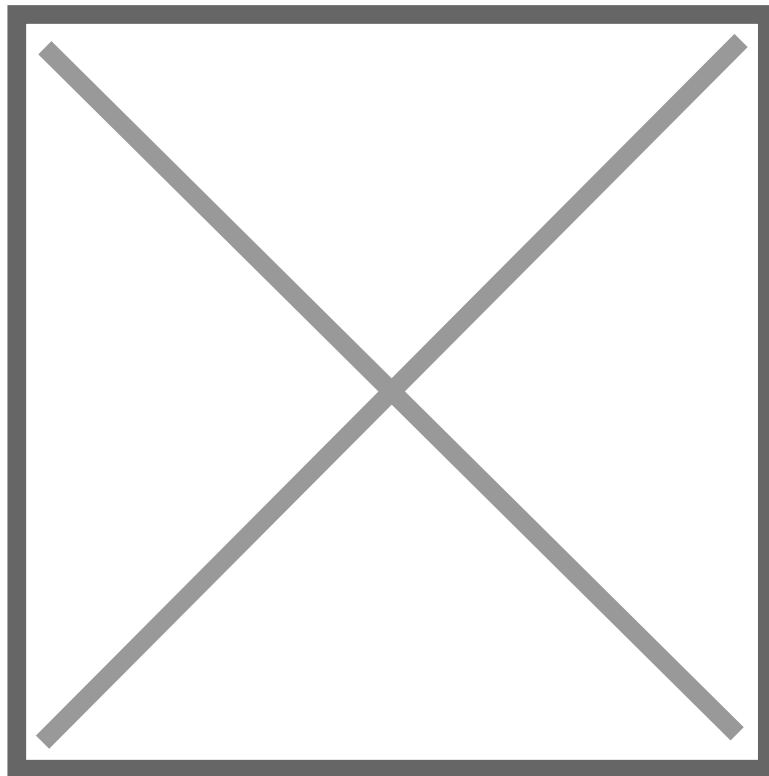


Figure 2: Camera

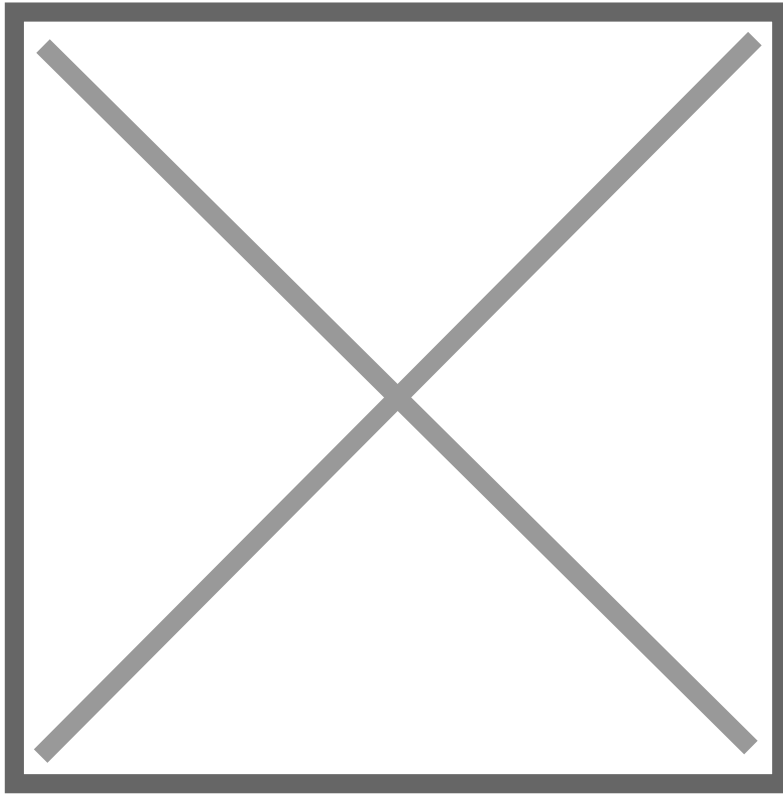


Figure 3: Camera

Revision #2

Created 4 April 2025 13:18:03 by Caroline

Updated 9 April 2025 16:58:49 by Caroline