

Creating Project

In this tutorial, you will **learn** how to develop a basic RISC-V project within the ChipInventor platform. We will cover everything from creating the project and setting up the main modules (processor, memories, registers, etc.) to simulating and testing on hardware. The structure follows the same pattern used in previous tutorials (such as “Distance Sensor” or “DC Motor”).

Initial Steps:

1. Open **ChipInventor**.
2. Click on **New Project**.
3. Fill in the fields:
 - **Name:** RISC-V Basic
 - **Description:** Basic project to understand the RISC-V architecture in ChipInventor
 - **Type:** FPGA
4. Click **Create** to generate the new project.

This environment is where you will add all the blocks (Verilog modules) and make the necessary connections to have a functional RISC-V system.

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